


Arthur Hlaing

2080 Tilia Street Apt 342A

Davis, CA 95616

☎ 909-684-3510

athlaing.me 

athlaing@ucdavis.edu 

athlaing 

athlaing 

Education

M.S. in Computer Engineering

University of California, Davis

GPA: -

09/2018 - present

B.S. in Electrical Engineering

University of California, Davis

GPA: 3.74

09/2014 - 06/2018

Experience

VLSI Computation Laboratory

Electrical and Computer Engineering at University of California, Davis

06/2017 - present

Low-Power Implementation of AlexNet on AsAP Architecture

- * Implemented pooling layers of AlexNet on AsAP simulator

AsAP Mapper

- * Developed visualization tools for post placement and routing of fine-grain many core architectures (AsAP3 KiloCore and AsAP4)
- * Wrote an initial placement algorithm that bipartite matches an AsAP application task graph to an AsAP simulated architecture
- * Simulated mappings with different parallel processing architectures and interconnect topologies to measure their impact on mapping quality

Micro Nano Innovations Laboratory

Biomedical Engineering at University of California, Davis

11/2016 - 04/2017

Cloud Pipette Project

- * Developed an iOS application that accepts laboratory pipette volume inputs and transmits them via Bluetooth to an automated laboratory pipette
- * Integrated Firebase Realtime Database into the iOS application for real-time data backup

Personal Project

06/2018 - present

Convolutional Neural Networks

- * Implementing MNIST Digit Recognizer in Verilog using 16-bit bfloat arithmetic
- * Implemented AlexNet and MNIST Digit Recognizer CNNs in C

Skills

Software: C, C++, Verilog, MATLAB, Python, Julia, Swift, JavaScript